IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Filing Date June 19, 2001 Examiner K. Duda

Title: Semiconductor Processing Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the reference listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of this patent is respectfully requested.

Respectfull submitted.

Attornev:

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